

● General Description

It combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

● Features

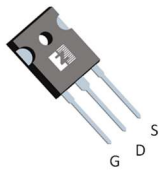
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low thermal resistance
- AEC-Q101 qualified

● Application

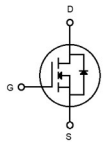
- BLDC motor driver
- DC-DC
- Load switch



● Product Summary



TO-247



$V_{DS}=200V$

$R_{DS(ON)}=7.2m\Omega$

$I_D=161A$



● Ordering Information

Part NO.	ZMSA070N20HC
Marking	ZMS070N20H
Packing information	TUBE BULK
Basic ordering unit (pcs)	400

● Absolute Maximum Ratings ($T_A=25^\circ C$, unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Max.	Unit
Drain-source voltage	V_{DS}		-	200	V
Gate-source voltage ^①	V_{GS}		-20	20	V
Continuous drain current	I_D	$V_{GS}=10V, T_C=25^\circ C$	-	161	A
	I_D	$V_{GS}=10V, T_C=75^\circ C$	-	132	A
	I_D	$V_{GS}=10V, T_C=100^\circ C$	-	114	A
Pulsed drain current ^①	I_{DM}	Pulsed; $t_p \leq 10 \mu s; T_C = 25^\circ C$;	-	644	A
Total power dissipation	P_D	$T_C=25^\circ C$	-	625	W
Total power dissipation	P_D	$T_A=25^\circ C$	-	3.8	W
Operating junction temperature	T_J		-55	175	$^\circ C$
Storage temperature	T_{STG}		-55	175	$^\circ C$
Single pulse avalanche energy	E_{AS}	$L=0.1mH, V_{GS}=10V, R_g=25\Omega$,	-	911	mJ
		$L=0.5mH, V_{GS}=10V, R_g=25\Omega$,	-	1640	mJ
ESD level (HBM)			CLASS 2		

● Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R_{thJC}	-	-	0.24	°C/W
Thermal resistance, junction - ambient	R_{thJA}	-	-	40	°C/W
Soldering temperature	T_{sold}	-	-	260	°C

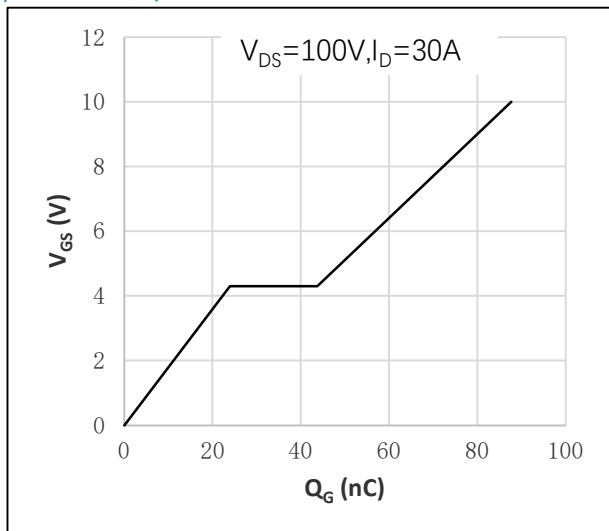
● Electronic Characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain-source breakdown voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	200	-	-	V
Gate threshold voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	2	2.8	4	V
Drain-source leakage current	I_{DSS}	$V_{GS}=0V, V_{DS}=200V$	-	-	1	μA
Gate- source leakage current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	100	nA
Static drain-source on resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=30A, T_J=25^{\circ}\text{C}$	-	7.2	8.6	m Ω
		$V_{GS}=10V, I_D=30A, T_J=175^{\circ}\text{C}$	-	20.1	-	m Ω
Forward transconductance	g_{FS}	$V_{DS}=5V, I_{SD}=10A$	-	38	-	S
Diode forward voltage	V_{FSD}	$V_{GS}=0V, I_{SD}=30A$	-	-	1.3	V

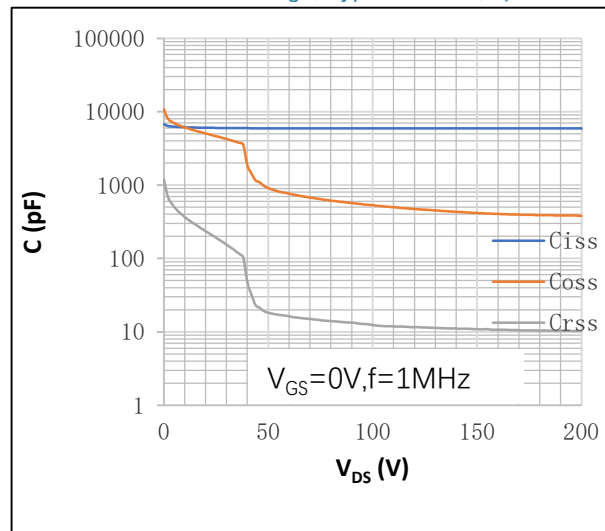
● Dynamic characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Input capacitance	C_{iss}	$f=1\text{MHz}, V_{DS}=100V, V_{GS}=0V$	-	5944	-	pF
Output capacitance	C_{oss}		-	537	-	pF
Reverse transfer capacitance	C_{rss}		-	13	-	pF
Gate resistance	R_g	$f=1\text{MHz}$	-	0.9	-	Ω
Total gate charge	Q_g	$V_{DD}=100V, I_D=30A, V_{GS}=10V$	-	87.7	-	nC
Gate-source charge	Q_{gs}		-	24	-	nC
Gate-drain charge	Q_{gd}		-	19.8	-	nC
Turn-on delay time	$t_{D(on)}$	$V_{GS}=10V, V_{DS}=100V, R_G=3.3\Omega, I_D=30A$	-	14	-	ns
Turn-on rise time	t_r		-	29	-	ns
Turn-off delay time	$t_{D(off)}$		-	27	-	ns
Turn-off fall time	t_f		-	10	-	ns
Reverse recovery time	t_{rr}	$V_{DD}=100V, dI_S/dt=100A/\mu s, I_S=30A$	-	29	-	ns
Reverse recovery charge	Q_{rr}		-	292	-	nC

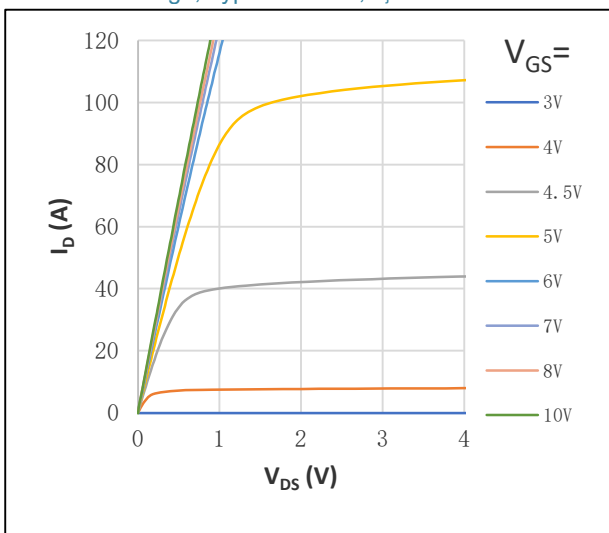
● Fig.1 Gate-source voltage as a function of gate charge; Typical values; $T_J=25^\circ\text{C}$



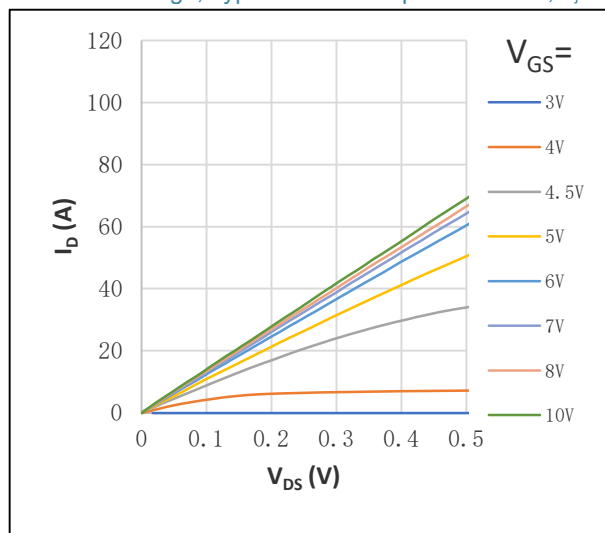
● Fig.2 Input, output and reverse transfer capacitances as a function of drain-source voltage; Typical values; $T_J=25^\circ\text{C}$



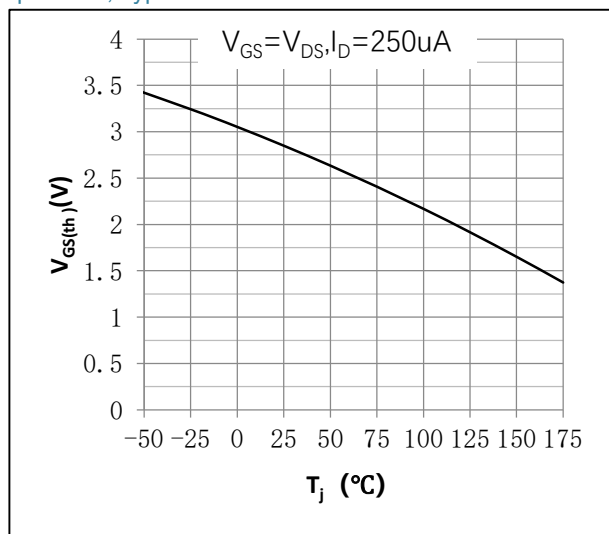
● Fig.3 Output characteristics: drain current as a function of drain-source voltage; Typical values; $T_J=25^\circ\text{C}$



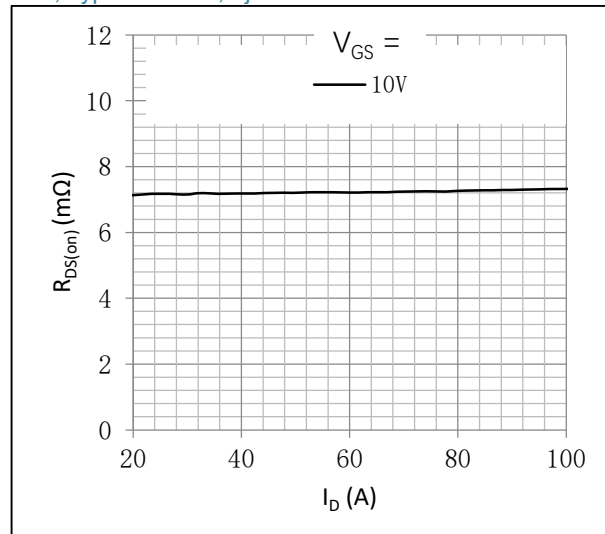
● Fig.4 Output characteristics: drain current as a function of drain-source voltage; Typical values: Expanded curve; $T_J=25^\circ\text{C}$



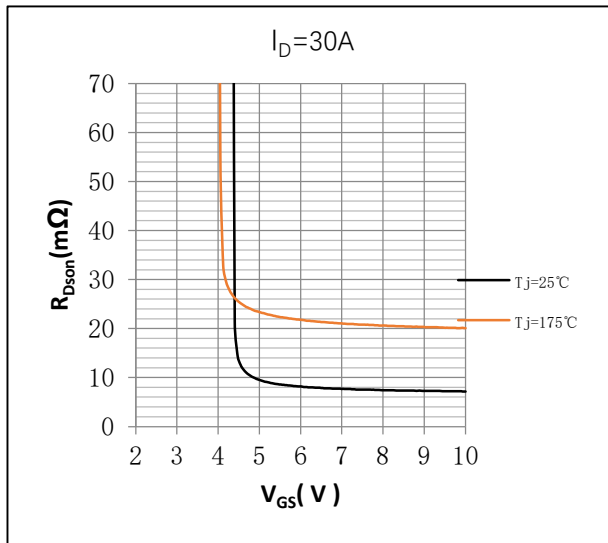
● Fig.5 Gate-source threshold voltage as a function of junction temperature; Typical values



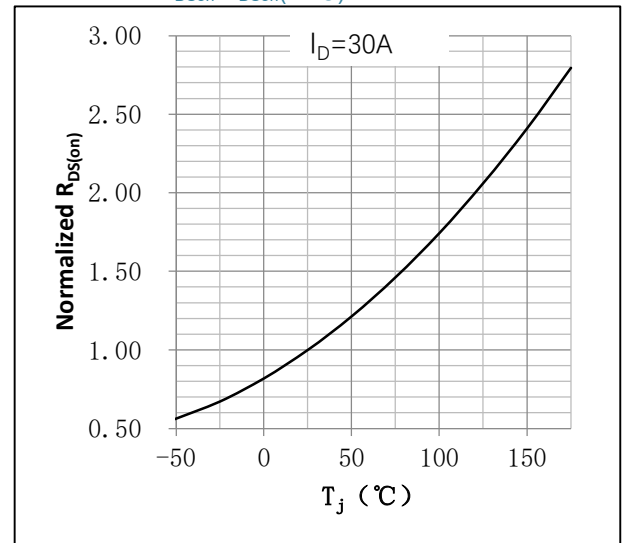
● Fig.6 Drain-source on-state resistance as a function of drain current; Typical values; $T_J=25^\circ\text{C}$



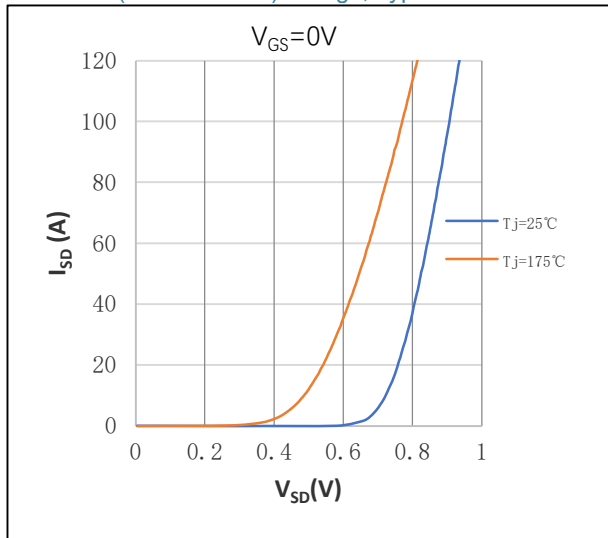
● Fig.7 Drain-source on-state resistance as a function of gate-source voltage; Typical values



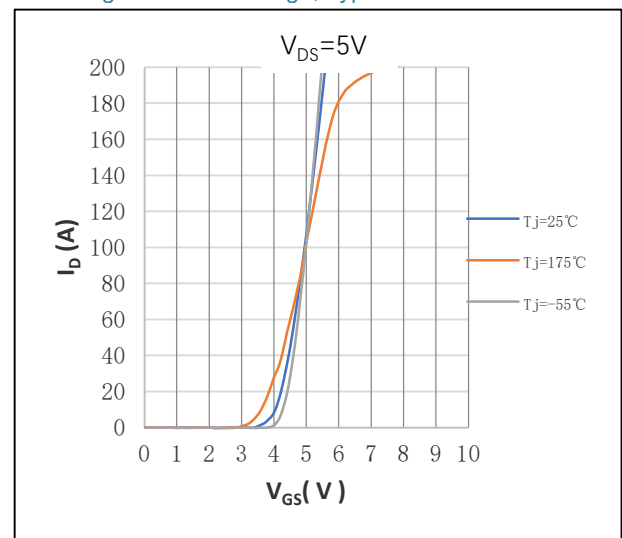
● Fig.8 Normalized drain-source on-state resistance factor as a function of junction temperature; Typical values Normalized On-Resistance = $R_{DS(on)}/R_{DS(on)}(25^{\circ}\text{C})$



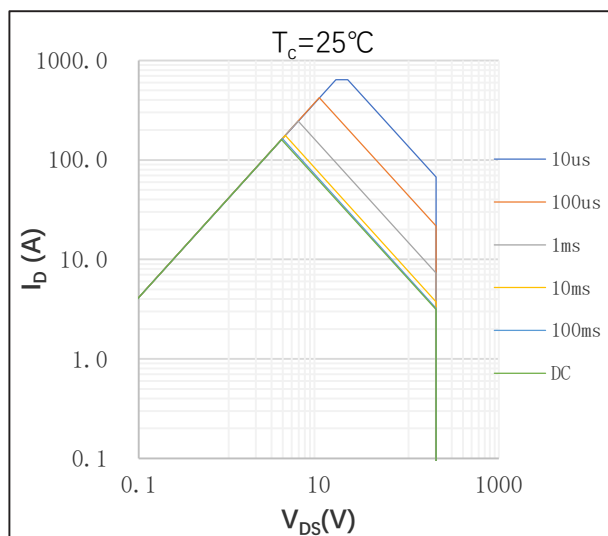
● Figure 9. Source (diode forward) current as a function of source-drain (diode forward) voltage; Typical values



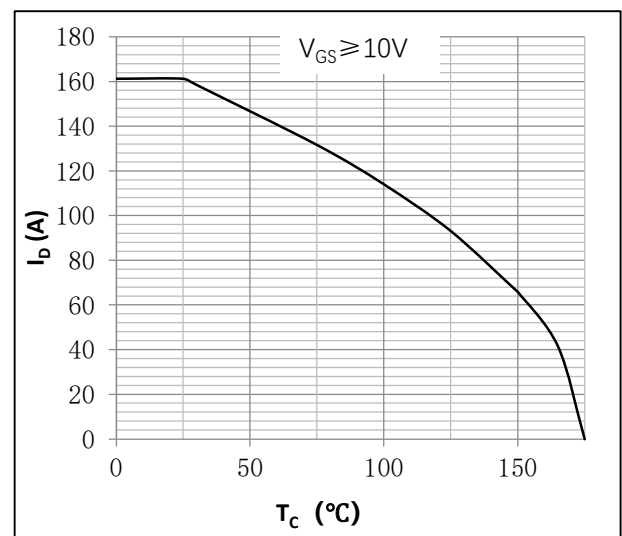
● Figure 10. Transfer characteristics: drain current as a function of gate-source voltage; Typical values



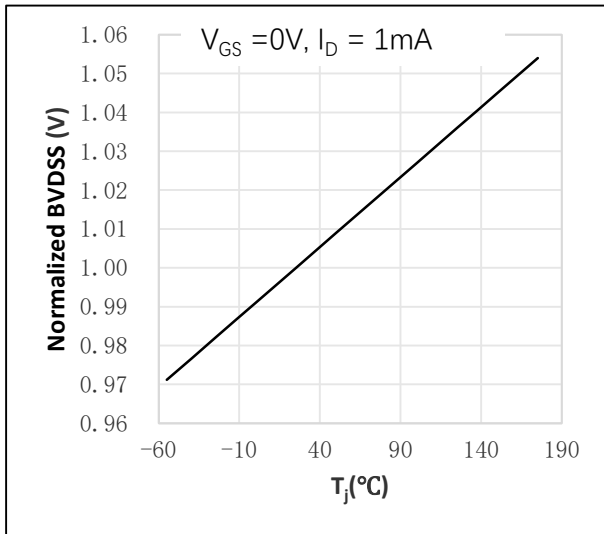
● Fig.11 Safe operating area: continuous and peak drain currents as a function of drain-source voltage; Calculative values



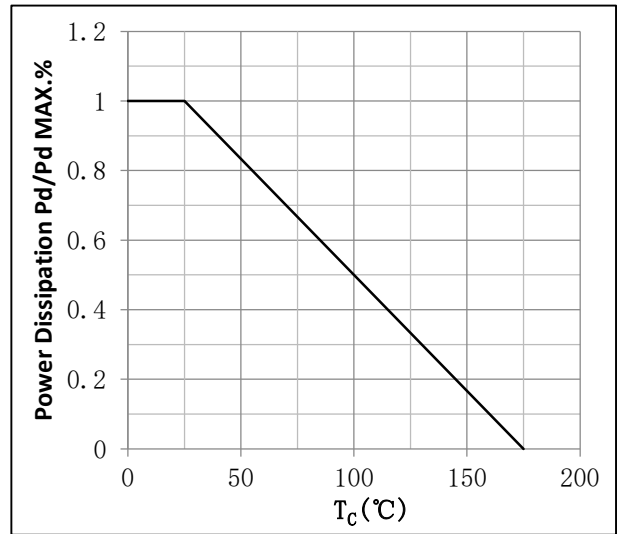
● Fig.12 Continuous drain current as a function of case temperature²; Calculative values



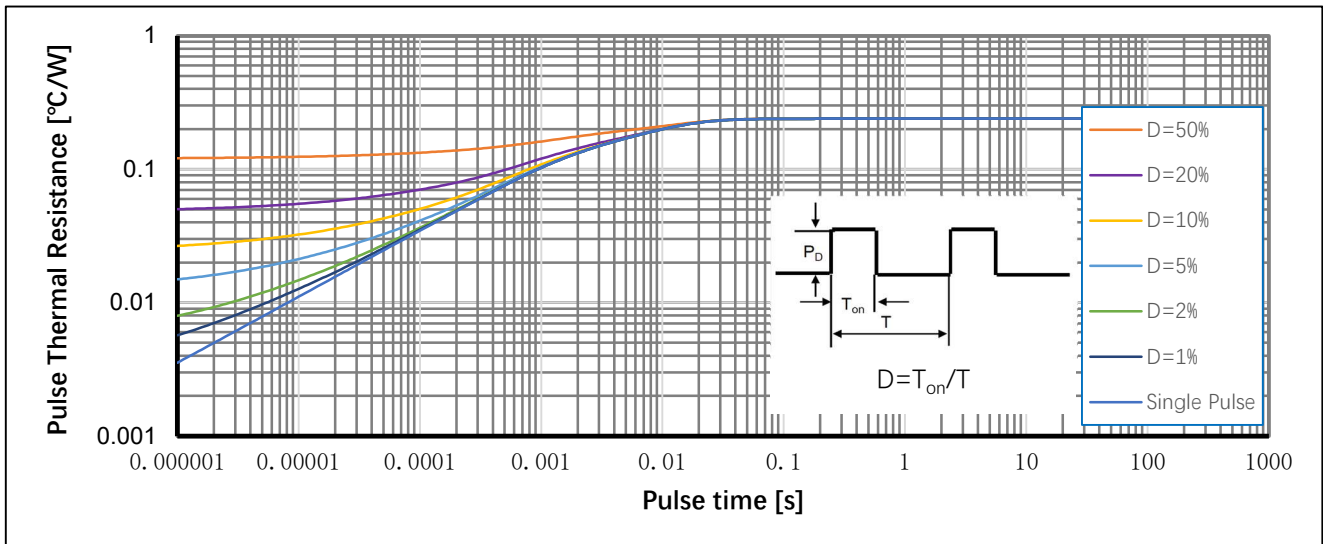
● Fig.13 Drain-source breakdown voltage as a function of junction temperature; Typical values Normalized $BV_{DSS} = BV_{DSS}/BV_{DSS}(25^{\circ}\text{C})$



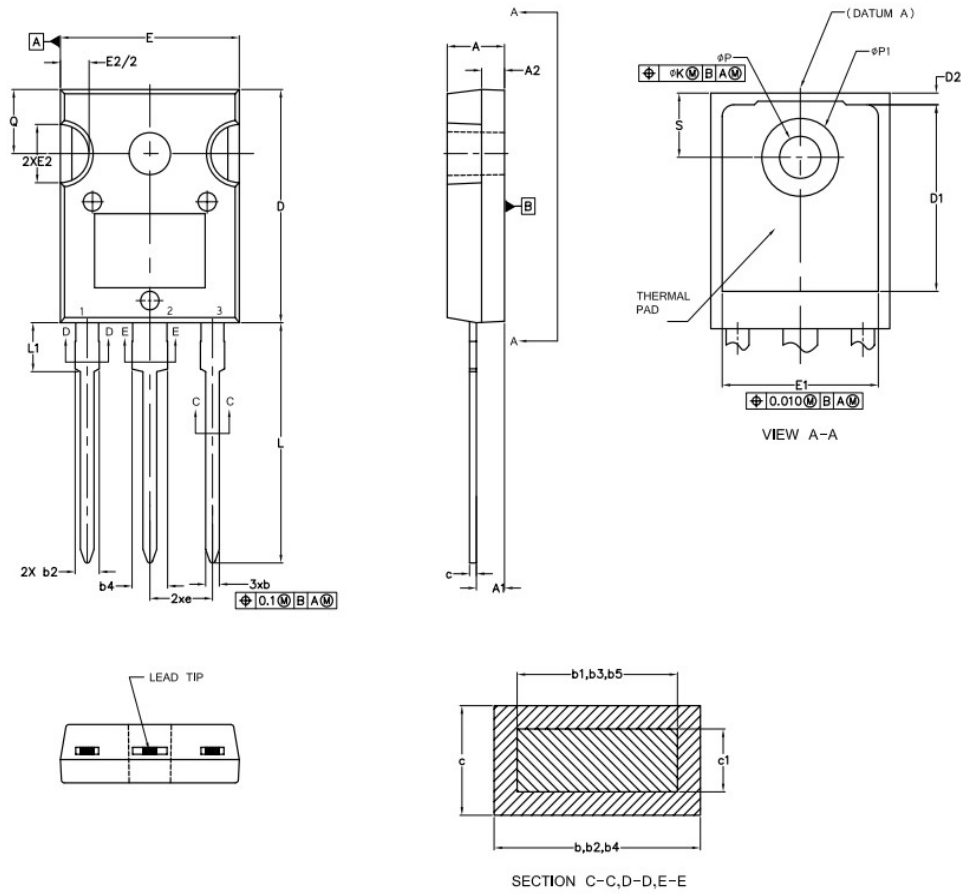
● Fig.14 Normalized total power dissipation as a function of case temperature; Calculative values Normalized Power Dissipation $= P_d/P_d(25^{\circ}\text{C})$



● Fig.15 Transient thermal impedance from junction to case as a function of pulse duration; max values



● Package Outline



SYMBOLS	DIMENSIONS			
	mm		inch	
	MIN.	MAX.	MIN.	MAX.
A	4.83	5.13	0.190	0.20
A1	2.21	2.59	0.087	0.102
A2	1.50	2.49	0.059	0.098
b	0.99	1.40	0.039	0.055
b1	0.99	1.35	0.039	0.053
b2	1.65	2.39	0.065	0.094
b3	1.65	2.34	0.065	0.092
b4	2.59	3.43	0.102	0.135
b5	2.59	3.38	0.102	0.133
c	0.38	0.89	0.015	0.035
c1	0.38	0.84	0.015	0.033
D	19.71	20.70	0.776	0.815
D1	13.08	—	0.515	—
D2	0.51	1.35	0.020	0.053
E	15.29	15.87	0.602	0.625
E1	13.46	—	0.530	—
E2	4.52	5.49	0.178	0.216
e	5.46BSC		0.215BSC	
L	19.57	21.00	0.780	0.827
L1	3.71	4.29	0.146	0.169
φP	3.56	3.66	0.140	0.144
φP1	—	7.39	—	0.291
Q	5.31	5.69	0.209	0.224
S	5.51BSC		0.217BSC	

● Note

① Pulse : $V_{GS}=+20V/-20V$, Duty cycle=50%, $T_j=175^{\circ}C$, $t=1000$ hours; For DC , the following test conditions can be passed: $V_{GS}=+20V/-10V$, $T_j=175^{\circ}C$, $t=1000$ hours;

② Practically the current will be limited by PCB, thermal design and operating temperature. $V_{GS}=10V$.

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● Revision History

Version	Date	Change
A	2025/7/16	New